| <u> </u> | Application No. | | |
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| Notice of Allowability | Application No. | Applicant(s) | |
| | 09/660,324 | AHN ET AL. | |
| | Examin r | Art Unit | |
| | Fernandó L. Toledo | 2823 | |
| <u> 1900 - Nordan Janoba, marang mangkan kanang menggunan Negara kanang menggunan kanang meng</u> | T emando L. Toledo | 2023 | |
| The MAILING DATE of this communication app All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85 NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT F of the Office or upon petition by the applicant. See 37 CFR 1.31 | 6 (OR REMAINS) CLOSED in t i) or other appropriate commun RIGHTS. This application is su | this application. If not include | d |
| 1. This communication is responsive to <u>5/10/2004</u> . | | | |
| 2. The allowed claim(s) is/are 88,92-95,97-121 and 123. | | | |
| 3. The drawings filed on 12 September 2000 are accepted by | y the Examiner. | | |
| 4. ☐ Acknowledgment is made of a claim for foreign priority u a) ☐ All b) ☐ Some* c) ☐ None of the: | nder 35 U.S.C. § 119(a)-(d) or | (f). | • |
| 1. Certified copies of the priority documents hav | e been received. | | |
| 2. Certified copies of the priority documents hav | | No. | |
| 3. Copies of the certified copies of the priority do | | | on from the |
| International Bureau (PCT Rule 17.2(a)). | | ii ano national stage applicati | on nom the |
| * Certified copies not received: | • | | |
| Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. | of this communication to file a MENT of this application. | reply complying with the requ | uirements |
| 5. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which giv | nitted. Note the attached EXAM es reason(s) why the oath or d | IINER'S AMENDMENT or NC eclaration is deficient. | OTICE OF |
| 6. CORRECTED DRAWINGS (as "replacement sheets") mus | st be submitted. | • | |
| (a) including changes required by the Notice of Draftspers | son's Patent Drawing Review (| PTO-948) attached | |
| 1) 🗋 hereto or 2) 🗍 to Paper No./Mail Date | | | |
| (b) ☐ including changes required by the attached Examiner' Paper No./Mail Date | • | the Office action of | |
| Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in t | .84(c)) should be written on the | drawings in the front (not the b | eack) of |
| DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT | sit of BIOLOGICAL MATER | NAI must be submitted. No | ote the |
| | | | |
| | | | |
| Attachment(s) | | | |
| 1. Notice of References Cited (PTO-892) | 5. Notice of Infor | mal Patent Application (PTO- | 152) |
| 2. Notice of Draftperson's Patent Drawing Review (PTO-948) | 6. Interview Sum | mary (PTO-413), | • |
| Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date | 8), 7. ⊠ Examiner's An | all Date nendment/Comment | |
| 4. Examiner's Comment Regarding Requirement for Deposit | 8. 🗌 Examiner's Sta | atement of Reasons for Allow | ance |
| of Biological Material | | | |
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Application/Control Number: 09/660,324

Art Unit: 2823

EXAMINER'S AMENDMENT

informal

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

The application has been amended as follows:

In the claims:

88. A process for forming an interposer element for use as a chip carrier comprising the steps of:

providing an insulating layer on at least one surface of a silicon substrate; and

processing said insulating layer to produce at least one passive circuit element on or within said insulating layer, said at least one passive circuit element being separated from said silicon substrate by a portion of said insulating layer, said portion of said insulating layer having a thickness such that said at least one passive circuit element is electrically shielded from said silicon substrate,

solder bonding at least one integrated circuit chip to said interposer element, by forming a plurality of individual solder ball leads, wherein two or more of said individual solder ball leads use differing types of solder having differing melting points, such that said at least one integrated circuit chip is electrically connected [[connection]] to said at least one passive circuit element; and

forming a metallization pattern on or within said insulating layer, said metallization pattern being connected with said at least one passive circuit element.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Fernando L. Toledo whose telephone number is 571-272-1867. The examiner can normally be reached on Mon-Thu 7am to 5:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Olik Chaudhuri can be reached on 571-272-1855. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 8667217-9197 (toll-free).

George Fourson
Primary Examiner
Art Unit 2823

FToledo

13 May 2004